

PAT-NO: JP359111067A

DOCUMENT-IDENTIFIER: JP 59111067 A

TITLE: SPECTRUM DISPLAY APPARATUS OF AUDIO SIGNAL

PUBN-DATE: June 27, 1984

INVENTOR-INFORMATION:

NAME

TANAKA, YOSHIKI

INAMI, MAMORU

OTSUKI, YOSHIKI

ASSIGNEE-INFORMATION:

NAME

COUNTRY

VICTOR CO OF JAPAN LTD

N/A

APPL-NO: JP57221710

APPL-DATE: December 17, 1982

INT-CL (IPC): G01R023/16, H04S007/00

ABSTRACT:

PURPOSE: To enhance resolving power, by a method wherein an audio signal is converted to a digital signal and power spectrum operation is performed after FET operation while pattern information corresponding to the level of a spectrum is successively displayed at predetermined intervals by the display surface of display.

CONSTITUTION: Initialization is performed in a step 1 by closing a power source to clear an AD converter, RAM, V.RAM or the like and the register in a video display processor VDP is set at well as the setting of the region in V.ROM to be used or an operation mode is performed while a predetermined kind of pater information is transmitted to a pattern generator table PGT from ROM<SB>1</SB> through VDH. In addition, according to necessity, pattern information is transmitted to a sprite generator table SGT from ROM<SB>1</SB> and a sprite name or X-coordinates and color data are transferred to a sprite attribute table SAT. In this case, CPU repeatedly perform each step ranging from a step 2 to a step 9. As mentioned above, the multiplication order of CPU can be used in performing operation and spectrum display can be easily performed in high resolving capacity by simple constitution.

COPYRIGHT: (C)1984,JPO&Japio

----- KWIC -----

Abstract Text - FPAR (2):

CONSTITUTION: Initialization is performed in a step 1 by closing a power source to clear an AD converter, RAM, V.RAM or the like and the register in a video display processor VDP is set as well as the setting of the region in V.ROM to be used or an operation mode is performed while a predetermined kind of pattern information is transmitted to a pattern generator table PGT from ROM<SB>1</SB> through VDH. In addition, according to necessity, pattern information is transmitted to a sprite generator table SGT from ROM<SB>1</SB> and a sprite name or X-coordinates and color data are transferred to a sprite attribute table SAT. In this case, CPU repeatedly perform each step ranging from a step 2 to a step 9. As mentioned above, the multiplication order of CPU can be used in performing operation and spectrum display can be easily performed in high resolving capacity by simple constitution.